

FIG.2

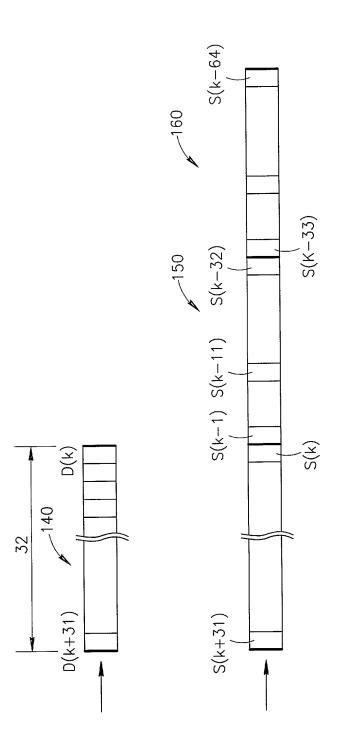
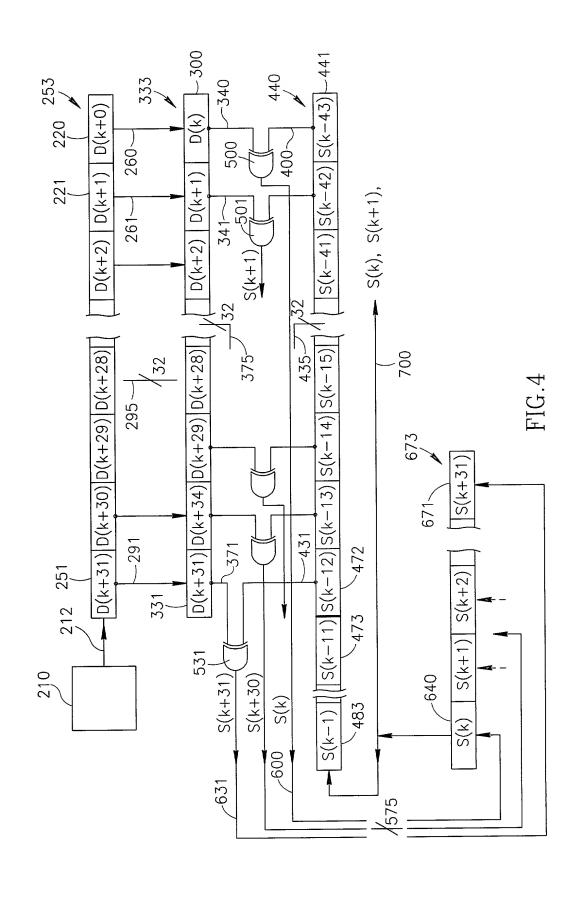


FIG.3



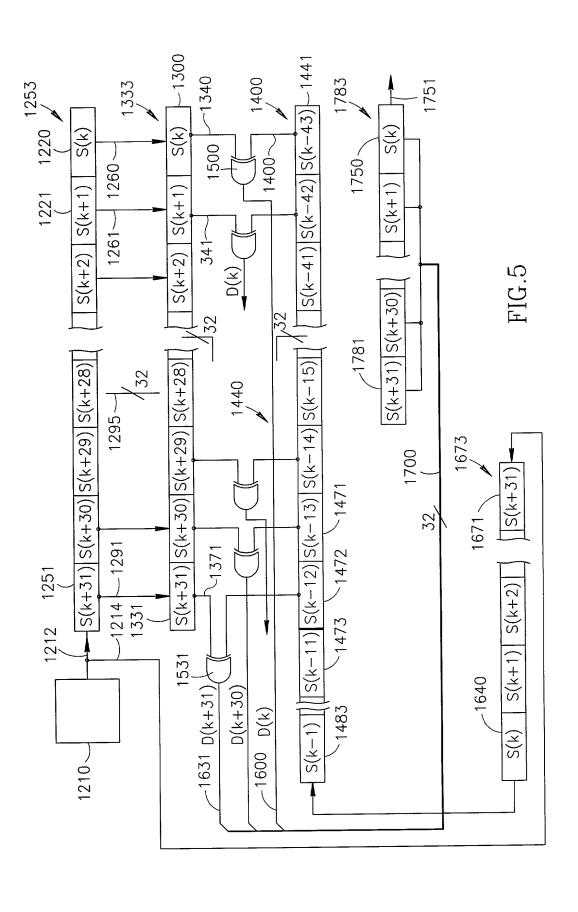
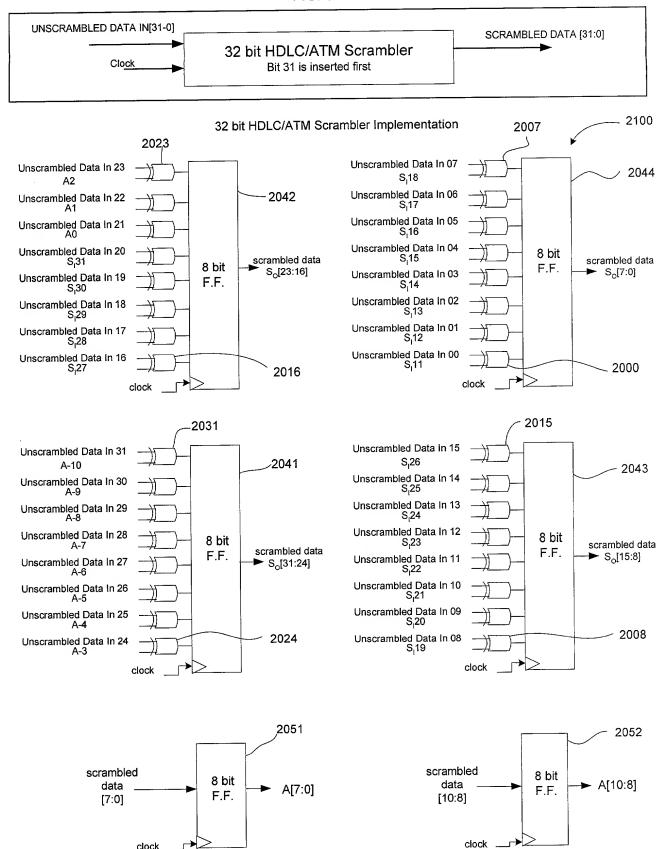


FIG. 6



SCRAMBLED DATA IN[31-0]

32 bit HDLC/ATM De-Scrambler
Bit 31 is inserted first

32 bit HDLC/ATM De-Scrambler Implementation
2507
2523

